

ISSUE CLASSIFICATION	
Class	Subclass

PD O.I.P.E. <i>LM2</i> SCANNED <i>LM2</i> O.A. <i>LM2</i>	PATENT DATE
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APPLICATION NO. 09/751377	CONT/PRIOR	CLASS 712	SUBCLASS 218	ART UNIT 2183	EXAMINER
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Anthony Jarvis

Bypass circuitry for use in a pipelined processor

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS <div>Sheets Drwg.</div> <div>Figs. Drwg.</div> <div>Print Fig.</div>		CLAIMS ALLOWED	
			<div>Total Claims</div>	<div>Print Claim for O.G.</div>
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	<div>_____ (Assistant Examiner) _____ (Date)</div>		NOTICE OF ALLOWANCE MAILED <div>_____</div>	
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